

FIG. 1

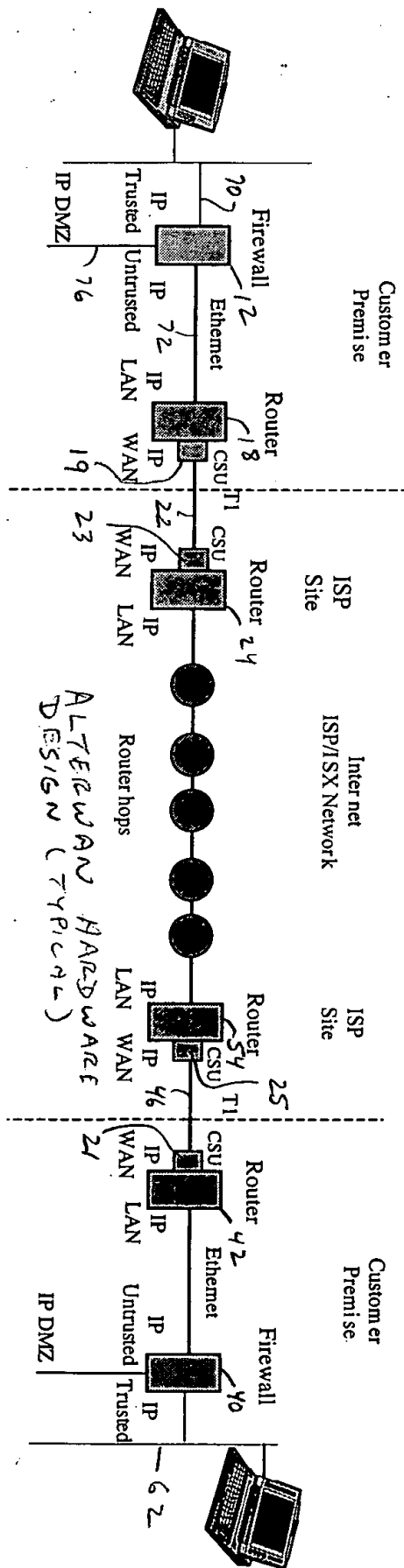
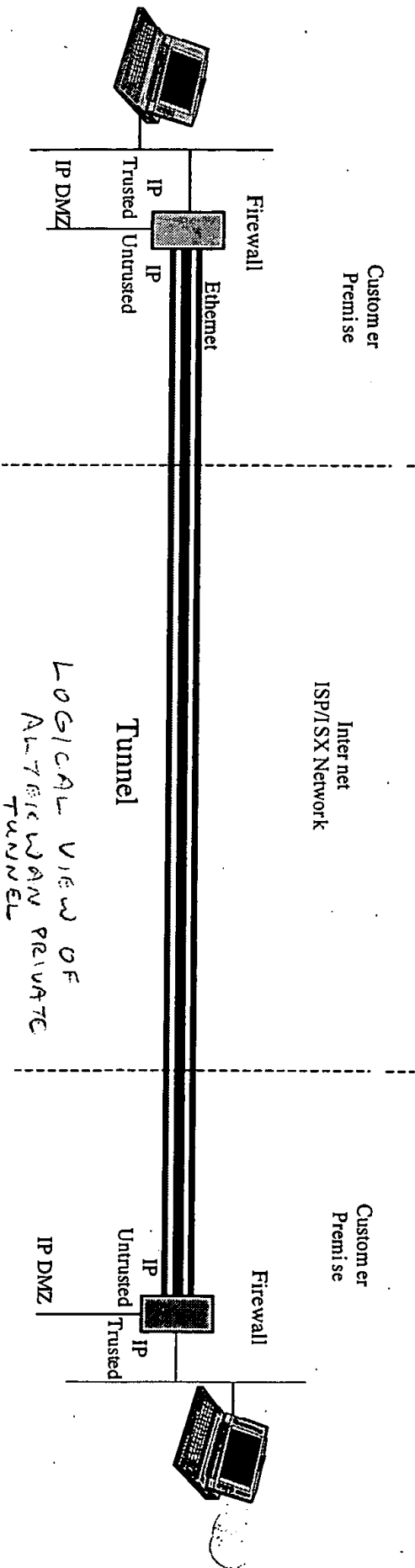


Fig. 2

ALTERMAN HARDWARE DESIGN (TYPICAL)



7.6.3

LOGICAL VIEW OF ATM NETWORK PRIVATE TUNNEL